

REMARKS

Favorable consideration of this application as presently amended and in light of the following discussion is respectfully requested.

Claims 1-52 are pending in the application, with Claims 1-31, 33, 35, 36, 38, 40-41, 43-45, and 47-52 amended by the present amendment.

In the Official Action, Claims 1-52 were objected to; Claims 31-35, 36-40, 49 and 50 were rejected under 35 U.S.C. § 112, second paragraph; Claims 1, 3-4, 6, 13-14, 16, 23, and 25 were rejected under 35 U.S.C. § 102(a) as being anticipated by applicants' admitted Prior Art; Claims 2, 5, 7-12, 15, 17-22, 24, and 26-52 were indicated as allowable.

Applicants acknowledge with appreciation the indication of allowable subject matter.

Claims 1-31, 33, 35, 36, 38, 40-41, 43-45, and 47-52 are amended to correct the informalities noted in the Official Action. In addition, the specification and abstract are amended, Figures 1-2 are amended, and Figures 11-12 are added. Figures 11-12 correspond to original Figures 1-2, with the label prior art removed. Original Figures 1-2 are amended to remove the conflict management circuit 60. Corresponding changes are provided in the specification. As noted in the attached declaration, the inventors incorrectly identified Figures 1-2 as prior art. Applicants were, and still are, aware of no device similar to the devices shown in original Figures 1-2 and now shown in new Figures 11-12. No new matter is added.

As noted in the attached declaration, Applicants erroneously labeled Figures 1-2 as prior art. Thus, with Figures 1-2 amended so as not to show a conflict management circuit 60 between two N-port decoders, Applicants submit that Applicants Admitted Prior Art does not teach or suggest the inventions recited in Claims 1, 3-4, 6, 13-14, 16, 23, and 25.

Furthermore, independent Claims 1, 31, 36, 41, 45, 49-52 each recite "N-port data memories each comprising M-number of one-port cell blocks." However, original Figures 1-

2 show that the memories are multi-port cells, which differ from one-port cell blocks. Also, independent Claims 1, 31, 36, 41, 45, 49-52 each recite "a conflict management circuit for managing write and read conflicts." Applicants further note that Figures 4 and 6 are labeled to show that the conflict management circuit for managing write and read conflicts.

However, original Figures 1-2 are labeled to show that only write conflicts are managed.

Therefore, Applicants submit the inventions defined by Claims 1, 31, 36, 41, 45, 49-52, and all claims depending therefrom, are not anticipated and are not rendered obvious by the asserted prior art for at least the reasons stated above.¹

Accordingly, in view of the present amendment and in light of the previous discussion, Applicants respectfully submit that the present application is in condition for allowance and respectfully request an early and favorable action to that effect.

Respectfully submitted,

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¹ MPEP § 2142 "...the prior art reference (or references when combined) must teach or suggest **all** the claim limitations. The teaching or suggestion to make the claimed combination and the reasonable expectation of success must both be found in the prior art, and not based on applicant's disclosure. In re Vaeck, 947 F.2d 488, 20 USPQ2d 1438 (Fed. Cir. 1991)."